

AMENDMENTS TO THE CLAIMS

1. (Original) A nitride-based semiconductor element comprising:
a plurality of mask layers formed at a prescribed interval to come into contact with the upper surface of an underlayer while partially exposing said underlayer; and
a nitride-based semiconductor layer, formed on the upper surface of said underlayer and said mask layers, consisting of a material different from that of said underlayer, wherein
the minimum distance between adjacent said mask layers is smaller than the width of an exposed part of said underlayer located between said adjacent mask layers.
2. (Original) The nitride-based semiconductor element according to claim 1, wherein
said underlayer includes a substrate, and
said mask layers are formed to be in contact with the upper surface of said substrate.
3. (Currently amended) The nitride-based semiconductor element according to claim 1, wherein further comprising facets[[,]] of the nitride-based semiconductor layer were formed on an exposed part of the upper surface of said underlayer located between said adjacent mask layers, having at least two types of different sizes.

4. (Original) The nitride-based semiconductor element according to claim 1, wherein

said nitride-based semiconductor layer is formed on the upper surface of said underlayer through a buffer layer.

5. (Original) The nitride-based semiconductor element according to claim 1, wherein

said nitride-based semiconductor layer is formed to be in contact with the upper surface of said underlayer.

6. (Original) The nitride-based semiconductor element according to claim 1, wherein

said mask layers have overhangs protruding above an exposed part of said underlayer.

7. (Original) The nitride-based semiconductor element according to claim 6, wherein

said mask layers are at least partially inverse-trapezoidal.

8. (Currently amended) The nitride-based semiconductor element according to claim 1, wherein

said underlayer includes projection portions,

said projection portions are inverse-trapezoidal convex portions, and

said mask layers are formed to be in contact with the upper surfaces of said inverse-trapezoidal convex portions.

9. (Original) The nitride-based semiconductor element according to claim 1, wherein

said underlayer includes projection portions, and

said mask layers are formed to be in contact with the upper surfaces of said projection portions so that side portions of said mask layers protrude from said projection portions.

10. (Original) The nitride-based semiconductor element according to claim 1, wherein

said mask layers include:

a first mask layer formed to be in contact with the upper surface of said underlayer, and

a second mask layer, formed on said first mask layer, consisting of a material harder to etch than said first mask layer.

11. (Original) The nitride-based semiconductor element according to claim 1, further comprising a nitride-based semiconductor element layer, formed on said nitride-based semiconductor layer, having an element region.

12. (Withdrawn) A method of forming a nitride-based semiconductor comprising steps of:

forming a plurality of mask layers at a prescribed interval to be in contact with the upper surface of an underlayer while partially exposing said underlayer; and

growing a nitride-based semiconductor layer consisting of a material different from that of said underlayer on the upper surface of said underlayer and said mask layers, wherein

the minimum distance between adjacent said mask layers is smaller than the width of an exposed part of said underlayer located between said adjacent mask layers.

13. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said step of growing said nitride-based semiconductor layer includes a step of growing said nitride-based semiconductor layer on the upper surface of said underlayer through a buffer layer.

14. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said step of growing said nitride-based semiconductor layer includes a step of growing said nitride-based semiconductor layer to be in contact with the upper surface of said underlayer.

15. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said mask layers have overhangs protruding above an exposed part of said underlayer.

16. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 15, wherein

said mask layers are at least partially inverse-trapezoidal.

17. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said underlayer includes a substrate, and

said mask layers are formed to be in contact with the upper surface of said substrate.

18. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said underlayer includes projection portions,

said projection portions are inverse-trapezoidal, and

said mask layers are formed to be in contact with the upper surfaces of said inverse-trapezoidal projection portions.

19. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said underlayer includes projection portions, and

said mask layers are formed to be in contact with the upper surfaces of said projection portions so that side portions of said mask layers protrude from said projection portions.

20. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, wherein

said step of forming said mask layers includes steps of:

forming a first mask layer to be in contact with the upper surface of said underlayer,

forming a second mask layer consisting of a material harder to etch than said first mask layer on said first mask layer, and

forming an etching mask on a prescribed region of said second mask layer and thereafter etching said second mask layer and said first mask layer through said etching mask serving as a mask thereby forming overhanging mask layers.

21. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 12, further comprising a step of growing a nitride-based semiconductor element layer having an element region on said nitride-based semiconductor layer.

22. (Withdrawn) A method of forming a nitride-based semiconductor comprising steps of:

forming a plurality of mask layers at a prescribed interval to be in contact with the upper surface of an underlayer while partially exposing said underlayer; and

growing a nitride-based semiconductor layer consisting of a material different from that of said underlayer on an exposed part of the upper surface of said underlayer located between adjacent said mask layers to have difference in growth rate.

23. (Withdrawn) The method of forming a nitride-based semiconductor according to claim 22, wherein

said step of growing said nitride-based semiconductor layer includes a step of growing facets having at least two types of different sizes on said exposed part of the upper surface of said underlayer located between said adjacent mask layers thereby growing said nitride-based semiconductor layer.